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Software Release Note
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RELEASE DATE: 04-Jun-2009
FIRMWARE VERSION: V1.08
KERNEL VERION: V2.01

FIRMWARE CHANGES AND ENHANCEMENTS:

N/A

MODIFICATIONS:
Note!! The following Known Issues have been fixed in this release of firmware.

Add Q-in-Q function into this version.
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KNOWN RESTRICTIONS AND LIMITATIONS:

N/A

KNOWN ISSUES:

Note!!The following Known Issues will fix in the further release of firmware.

N/A

Configuration note & Test procedure:

Purpose:
Verify Q-in-Q VLAN function of MIL-SM8TXAF2GPA.

Resource requirements:
1. MIL-SM8TXAF2GPA *2 (DUT A, B,)
2. Device which can produce tagged frames (ex: IXIA)

Last modification:
Jun/4th/2009

Test Setup & Topology:
1. Connect DUT A port #1 to DUT B port #1.(VLAN Mode: Trunk Link)
2. Connect DUT A port #2 & DUT B port #2 to IXIA.
3. Connect DUT A port #3 & DUT B port #3 to IXIA.(capture Port #3 packets by IXIA)
4. IXIA transmits packets between DUT A & B.

Q-in-Q frame diagram (double tagged):

DA	SA	8100	2nd (outer) tag, Provider VLAN (10)	8100	1st (inner) tag, Customer VLAN (100)	Len/Etype	Data	FCS

Configuration of DUT A & B:

1. Select VLAN Operation Mode: 802.1Q at VLAN Configuration web page.
2. The setting is as below:

Note:

- *The value of "Trunk Link tagged VID (10)" is the provider's VLAN ID value, the 2nd tag (outer tag).*

3. Connect IXIA to DUT A、B port #3, and then enable Port Mirroring on DUT A、B like below**.**

Test Procedure:

Step-1.Producing Tagged frames (Tag 100) by IXIA. (1st tag)

Step-2.Captured DUT A、B port #3 packets by IXIA.